



Name: Pankaj Vishwakarma

Designation: Assistant Professor

Department: Electronics and Communication Engineering

Phone No(s): 7376976015

Email: pv1409@gmail.com

Qualifications: M. Tech

Experience: 8Years

**Area of Interest: VLSI Technology, Microprocessor and
Microcontroller, Electromagnetic Field Theory**

Publication:

Book/Book Chapter:

Patent:

Sponsored Projects:

Consultancy:

CURRICULUM VITAE

PANKAJ VISHWAKARMA

Address for correspondence:

Bhelkha SAMS Road
Chamaon
Shivpur, Varanasi UP-221003
Ph. No: 07376976015(self)
Email id: pv1409@gmail.com
pv1409@live.com

Career Objective

To be associated with a progressive organization which can provide me a dynamic work sphere to extract my inherent skills as a Professional.

Educational Qualifications

- Completed M.Tech(2013-15) in **VLSI Design** from Galgotias University with 6.79(CGPA)(67.9%).
- Completed B.Tech in **Electronics and Communication Engineering** from Babu Banarasi Das Institute of Engineering Technology & Research Centre with 66.14% aggregate in 2010
- Completed XII standard in 2006 from H.A.L. School (CBSE), Korwa with 62.00% marks
- Completed X standard in 2004 from H.A.L. School (CBSE), Korwa with 69.40% marks

Experience

- Presently working as Asst. Prof. in **Ashoka Institute of Technology and Management, Varanasi (Feb-2023 to till date)**
- Worked as Asst. Prof. in **Rajarshi Rananjay Sinh institute of Management and Technology, Amethi (August-2016 to January-2023)**
- Worked as Asst. Prof. in **Shekhawati Engineering College, Dundlod (Rajasthan) (June-2015 to July-2016)**
- One year (Feb-2012 to Feb-2013) Graduate Apprentice Trainee in **Hindustan Aeronautics Limited, Avionics Division, Korwa**

Final year Project (M.Tech)

Project Title: Application of Autozeroing Floating Gate Amplifier (AFGA)

Abstract: Study of Autozeroing floating gate amplifier and used it in sense amplifier to reduce its noise or offset. AFGA is self adaptive circuit and reduce its input offset as natural part of its operation by modifying charge on floating gate.

Final Year Project(B.Tech)

Project name "**Secure data Transmission using Radio Frequency**"

Project Details:

- Transmission of data from one unit to another unit by using RF

- Data is transmitted using security code or in coded form
- At receiver side data can be decoded only by using predefined password i.e. only required user can decode the data.

Skill set

- EDA tool : OrCad Pspice, Eagle, Solidworks
- Technical subject: Microprocessor, Electromagnetic Field Theory, Analog Electronics, Digital Electronics, VLSI

Summer Training

45 days summer training at H.A.L. Avionics Division, Korwa in ‘**Assembly & Testing-Jaguar**’ department.

Extracurricular activities

- Member of Innovation Team & Encore at college under which different inter and intra college activities are managed.
- Presented seminar on Topic:
 1. Stealth Technology
 2. Nano Technology
- A participated in All India mathematics Olympiad at school level and got 68% marks

Personal Details

Father’s Name : Mr. M. C. Vishwakarma
Mother’s Name : Smt. Rajkumari
Date of Birth : 14th September 1989
Sex : Male
Marital Status : Unmarried
Nationality : Indian
Languages Known : Hindi & English
Hobbies : Drawing(pencil)

Declaration:

I hereby declare that above information is correct to the best of my knowledge and belief.

Date :

Place :

Pankaj Vishwakarma